



Attorney Docket No.: 333772000900
Application No.: 10/772,327

DECLARATION FOR UTILITY PATENT APPLICATION

AS A BELOW-NAMED INVENTOR, I HEREBY DECLARE THAT:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: METHOD AND APPARATUS FOR TESTING INTEGRATED CIRCUITS, the specification of which is attached hereto unless the following box is checked:

was filed on February 6, 2004 as United States Application Serial No. or PCT International Application No. 10/772,327 and was amended on
N/A

I HEREBY STATE THAT I HAVE REVIEWED AND UNDERSTAND THE CONTENTS OF THE ABOVE-IDENTIFIED SPECIFICATION, INCLUDING THE CLAIMS, AS AMENDED BY ANY AMENDMENT REFERRED TO ABOVE.

I acknowledge the duty to disclose information which is material to the patentability as defined in 37 C.F.R. § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

Application No.	Country	Date of Filing	Priority Claimed?
			<input type="checkbox"/> Yes <input type="checkbox"/> No

I hereby claim benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below:

Application Serial No.	Filing Date
60/447,839	2/14/2003
60/449,622	2/24/2003

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 C.F.R. § 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application.

Application Serial No.	Filing Date	Status
		<input type="checkbox"/> Patented <input type="checkbox"/> Pending <input type="checkbox"/> Abandoned
		<input type="checkbox"/> Patented <input type="checkbox"/> Pending <input type="checkbox"/> Abandoned

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under § 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

June 01, 2004 
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1 JUN 04 
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5/22/04 
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5/27/2004

Robert F. Sauer

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JUL 06 2004

PTO/SB/96 (08-03)

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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Ankan PRAMANICK et al.Application No./Patent No.: 10/772,327 Filed/Issue Date: February 6, 2004Entitled: METHOD AND APPARATUS FOR TESTING INTEGRATED CIRCUITSADVANTEST CORPORATION, a Japanese corporation
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. the assignee of the entire right, title, and interest; or
2. an assignee of less than the entire right, title and interest.

The extent (by percentage) of its ownership interest is _____ %
in the patent application/patent identified above by virtue of either:A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.**OR**B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.
2. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.
3. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

 Additional documents in the chain of title are listed on a supplemental sheet. Copies of assignments or other documents in the chain of title are attached.

NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

June 11, 2004

Date

MORITA Yuri

Typed or printed name

Telephone NumberSignatureManaging Executive Officer

Title

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: 6/12/2004 Mail Stop Missing Parts Signature: Christie Carter

**ASSIGNMENT
JOINT**

THIS ASSIGNMENT, by Ankan PRAMANICK, Mark ELSTON, Leon CHEN and Robert SAUER (hereinafter referred to as the assignors), each with a mailing address c/o Advantest America R&D Center, Inc., 3201 Scott Blvd., Santa Clara, California 95054, witnesseth:

WHEREAS, said assignors have invented certain new and useful improvements in Method and Apparatus for Testing Integrated Circuits, set forth in an application for Letters Patent of the United States, bearing Serial No. 10/772,327 and filed on February 6, 2004; and

WHEREAS, Advantest Corporation, a corporation duly organized under and pursuant to the laws of Japan and having its principal place of business at 1-32-1 Asahi-cho, Nerima-ku, Tokyo 179-0071, Japan (hereinafter referred to as the assignee) is desirous of acquiring the entire right, title and interest in and to said inventions and said application for Letters Patent of the United States, and in and to any Letters Patent or Patents, United States or foreign, to be obtained therefor and thereon:

NOW, THEREFORE, in consideration of One Dollar (\$1.00) and other good and sufficient consideration, the receipt of which is hereby acknowledged, said assignors have sold, assigned, transferred and set over, and by these presents do sell, assign, transfer and set over, unto said assignee, its successors, legal representatives and assigns, the entire right, title and interest in and to the above-mentioned inventions, application for Letters Patent, and any and all Letters Patent or Patents in the United States of America and all foreign countries which may be granted therefor and thereon, and in and to any and all divisions, continuations and continuations-in-part of said application, or reissues or extensions of said Letters Patent or Patents, and all rights under the International Convention for the Protection of Industrial Property, the same to be held and enjoyed by said assignee, for its own use and the use of its successors, legal representatives and assigns, to the full end of the term or terms for which Letters Patent or Patents may be granted, as fully and entirely as the same would have been held and enjoyed by the assignors, had this sale and assignment not been made.

AND for the same consideration, said assignors hereby covenant and agree to and with said assignee its successors, legal representatives and assigns, that, at the time of execution and delivery of these presents, said assignors are the sole and lawful owners of the entire right, title and interest in and to said inventions and the application for Letters Patent above-mentioned, and that the same are unencumbered and that said assignors have good and full right and lawful authority to sell and convey the same in the manner herein set forth.

AND for the same consideration, said assignors hereby covenant and agree to and with said assignee, its successors, legal representatives and assigns, that said assignors will, whenever counsel of said assignee, or the counsel of its successors, legal representatives and assigns, shall advise that any proceeding in connection with said inventions, or said application for Letters Patent, or any proceeding in connection with Letters Patent for said inventions in any country, including interference proceedings, is lawful and desirable, or that any division, continuation or continuation-in-part of any application for Letters Patent or any reissue or extension of any Letters Patent, to be obtained thereon, is lawful and desirable, sign all papers and documents, take all lawful oaths, and do all acts necessary or required to be done for the procurement, maintenance, enforcement and defense of Letters Patent for said inventions, without charge to said assignee, its successors, legal representatives and assigns, but at the cost and expense of said assignee, its successors, legal representatives and assigns.

AND said assignors hereby request the Commissioner of Patents to issue said Letters Patent of the United States to said assignee as the assignee of said inventions and the Letters Patent to be issued thereon for the sole use of said assignee, its successors, legal representatives and assigns.

June 01, 2004

Date


Ankan PRAMANICK

COPY

Attorney Docket No.: 333772000900
Application No.: 10/772,327

1 JUN 04
Date

Mark E
Mark ELSTON

5/22/04
Date

Leon Chen
Leon CHEN

5/27/04
Date

Robert F Sauer
Robert SAUER



Docket No.: 333772000900
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Ankan PRAMANICK

Application No.: 10/772,327

Group Art Unit: 2857

Filed: February 6, 2004

Examiner: Not Yet Assigned

For: **METHOD AND APPARATUS FOR TESTING
INTEGRATED CIRCUITS**

POWER OF ATTORNEY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The undersigned hereby appoints the following attorneys and/or agents to prosecute this application and transact all business in the U.S. Patent and Trademark Office connected herewith:

All practitioners at Customer Number 20872.

Address all communications to:

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425 Market Street
San Francisco, California 94105
(415) 268-6428

For: ADVANTEST CORPORATION

Dated: June 11, 2004